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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/512,405 ✓	
	Filing Date	June 15, 2005	
	First Named Inventor	Nobumitsu TAKASE	
	Art Unit	2812	
	Examiner Name	Not Yet Assigned	
Total Number of Pages in This Submission	21	Attorney Docket Number	474082002800

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/ Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <div style="border: 1px solid black; width: 100px; height: 50px; margin: 0 auto;"></div> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): Return Receipt Post Card Request for Corrected Filing Receipt Copy of OFR with corrections in red Request For Correct Notice Of Recordation Of Assignment (2 pages) Corrected Recordation Cover Sheet Copy of NOR w/corrections in red, copy of orig. Recordation Cover Sheet and Assignment and copy of Req. for Corrected OFR Sup. Application Data Sheet
<div style="border: 1px solid black; width: 100px; height: 20px; float: left; margin-right: 10px;"></div> Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	MORRISON & FOERSTER LLP		
Signature			
Printed name	Kevin R. Spivak		
Date	January 30, 2006	Reg. No.	43,148



Docket No.: 474082002800
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Nobumitsu TAKASE et al.

Application No.: 10/512,405

Confirmation No.: 4607

Filed: June 15, 2005

Art Unit: 2812

For: HIGH-RESISTANCE SILICON WAFER AND
ITS MANUFACTURING METHOD

Examiner: Not Yet Assigned

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby request that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicants, a copy of which is attached hereto, has an error in the title.

High resistance silicon wafer and method for production thereof should be:

High-resistance silicon wafer and **its manufacturing** method.

Applicants additionally request that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: January 30, 2006

Respectfully submitted,

By 

Kevin R. Spivak

Registration No.: 43,148

MORRISON & FOERSTER LLP

1650 Tysons Blvd, Suite 300

McLean, Virginia 22102

(703) 760-7762



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

KRS/XXR

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/512,405✓	06/15/2005	2812	1594	474082002800✓	6	17	4

CONFIRMATION NO. 4607

25227
 MORRISON & FOERSTER LLP
 1650 TYSONS BOULEVARD
 SUITE 300
 MCLEAN, VA 22102

FILING RECEIPT



OC000000016356604

Date Mailed: 06/24/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Nobumitsu Takase, Tokyo, JAPAN;
 Hideshi Nishikawa, Tokyo, JAPAN;
 Makoto Ito, Tokyo, JAPAN;
 Koji Sueoka, Tokyo, JAPAN;
 Shinsuke Sadamitsu, Tokyo, JAPAN;

RECEIVED
 JUN 30 2005
 MORRISON & FOERSTER LLP

Assignment For Published Patent Application

SUMITOMO MITSUBISHI SILICON CORPORATION, Minato-ku, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25227.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/04866 04/16/2003

Foreign Applications

JAPAN 2002-127509 04/26/2002
 JAPAN 2002-360731 12/12/2002

Projected Publication Date: 09/29/2005

Non-Publication Request: No

Early Publication Request: No

DOCKETED

Title

its manufacturing
 High-resistance silicon wafer and method for production thereof

Preliminary Class

438

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

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LICENSE FOR FOREIGN FILING UNDER

Title 35, United States Code, Section 184

Title 37, Code of Federal Regulations, 5.11 & 5.15

GRANTED

The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject



Docket No.: 474082002800
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Nobumitsu TAKASE et al.

Application No.: 10/512,405

Confirmation No.: 4607

Filed: June 15, 2005

Art Unit: 2812

For: HIGH-RESISTANCE SILICON WAFER AND
METHOD FOR PRODUCTION THEREOF

Examiner: Not Yet Assigned

REQUEST FOR CORRECTED NOTICE OF RECORDATION OF ASSIGNMENT

MS Assignment Recordation Services
Director of the US Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The Notice of Recordation of Assignment which was recorded on June 15, 2005 at Reel 016819, Frame 0225, has an error in the fourth assignor's name.

SUEOKA, KOUJI should be: SUEOKA, **KOJI**.

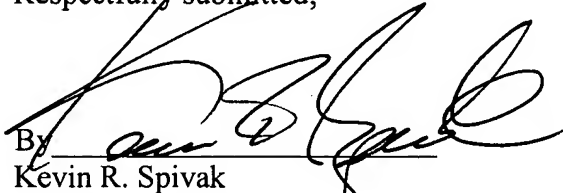
Attached is a copy of the Notice of Recordation of Assignment, a corrected Recordation Cover Sheet, a copy of the originally recorded cover sheet, and a copy of the Assignment. It is respectfully requested that the appropriate correction is made.

We are also attaching a copy of a Request for Corrected Filing Receipt filed concurrently with this request in which the title of the application is to be corrected. It is respectfully requested that the title on the Notice of Recordation also be corrected.

The error of the assignor's name occurred on our part and accordingly the fee of \$40.00 according to 37 CFR § 1.21(h) is due. The Commissioner is authorized to charge the recording fee to **Deposit Account No. 03-1952**, referencing docket no. **474082002800**.

Dated: January 30, 2006

Respectfully submitted,

By 

Kevin R. Spivak

Registration No.: 43,148
MORRISON & FOERSTER LLP
1650 Tysons Blvd, Suite 300
McLean, Virginia 22102
(703) 760-7762



UNITED STATES PATENT AND TRADEMARK OFFICE

UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

NOVEMBER 28, 2005

PTAS



103053393A

KEVIN R. SPIVAK
MORRISON & FOERSTER LLP
1650 TYSONS BLVD, SUITE 300
MCLEAN, VA 22102

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF
THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS
AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER
REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE
INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA
PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD
FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY
CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 571-272-3350.
PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE,
MAIL STOP: ASSIGNMENT SERVICES DIVISION, P.O. BOX 1450, ALEXANDRIA, VA 22313.

47408 - 20028.00

RECORDATION DATE: 06/15/2005

REEL/FRAME: 016819/0225

NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

TAKASE, NOBUMITSU

DOC DATE: 06/06/2005

ASSIGNOR:

NISHIKAWA, HIDESHI

DOC DATE: 06/03/2005

ASSIGNOR:

ITO, MAKOTO

DOC DATE: 06/06/2005

ASSIGNOR:

SUEOKA, ~~KOUJI~~ KOJI

DOC DATE: 06/08/2005

ASSIGNOR:

SADAMITSU, SHINSUKE

DOC DATE: 06/06/2005

KRS/mrh
DOCKETED

RECEIVED

DEC 13 2005

MORRISON & FOERSTER LLP

ASSIGNEE:

SUMITOMO MITSUBISHI SILICON
CORPORATION
2-1, SHIBAURA 1-CHOME, MINATO-KU
TOKYO, JAPAN 105-0023

SERIAL NUMBER: 10512405

FILING DATE: 06/15/2005

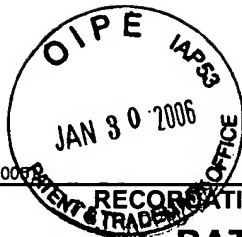
PATENT NUMBER:

ISSUE DATE:

TITLE: HIGH-RESISTANCE SILICON WAFER AND METHOD FOR PRODUCTION THEREOF

*its Manufacturing
(see attached copy of reg. for
Corrected Filing Receipt)*

MARY BENTON, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS



RECORDATION FORM COVER SHEET
PATENTS ONLY

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies)/Execution Date(s):

Nobumitsu TAKASE (06/06/2005), Hideshi NISHIKAWA
(06/03/2005), Makoto ITO (06/06/2005), Koji SUEOKA
(06/08/2005), and Shinsuke SADAMITSU (06/06/2005)

Execution Date(s): June 8, 2005

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of Conveyance:

- ☐ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Government Interest Assignment
☐ Executive Order 9424, Confirmatory License
☒ Other Corrected Recordation Form originally filed
on June 15, 2005 and recorded at Reel
016819/Frame 0225

2. Name and address of receiving party(ies)

Name: Sumitomo Mitsubishi Silicon Corporation

Internal Address: _____

Street Address: _____

Sumitomo Mitsubishi Silicon Corporation
2-1, Shibaura 1-chome, Minato-ku

City: Tokyo

State: _____

Country: Japan Zip: 105-0023

Additional name(s) & address(es)
attached: ☐ Yes ☒ No

4. Application or patent number(s):

A. Patent Application No.(s)
10/512,405

☐ This document is being filed together with a new application.

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address to whom correspondence
concerning document should be mailed:

Name: Kevin R. Spivak
MORRISON & FOERSTER LLP

Internal Address: Atty. Dkt.: 474082002800

Street Address: 1650 Tysons Blvd, Suite 300

City: McLean

State: VA Zip: 22102

Phone Number: (703) 760-7743

Fax Number: (703) 760-7777

Email Address: KSpivak@mofo.com

6. Total number of applications and
patents involved:

1

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00

- ☐ Authorized to be charged by credit card
☒ Authorized to be charged to deposit account
☐ Enclosed
☐ None required (government interest not affecting title)

8. Payment Information

- a. Credit Card Last 4 Numbers _____
Expiration Date _____
b. Deposit Account Number 03-1952
Authorized User Name Kevin R. Spivak

9. Signature:

Signature

January 30, 2006

Date

Kevin R. Spivak - 43,148

Name of Person Signing

Total number of pages including cover
sheet, attachments, and documents:

6

08-02-2005

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Form PTO-1595 (Rev. 09/04)
OMB No. 0651-0027 (exp. 6/30/2005)U.S. DEPARTMENT OF COMMERCE
United States Patent and Trademark Office

103053393

ET

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies)/Execution Date(s):

Nobumitsu TAKASE (06/06/2005), Hidashi NISHIKAWA
(06/03/2005), Makoto ITO (06/06/2005), Koji SUEOKA
(06/06/2005), and Shinsuke SADAMITSU (06/06/2005)

Execution Date(s): in parentheses after inventor name

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of Conveyance:

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Government Interest Assignment
☐ Executive Order 9424, Confirmatory License
☐ Other

2. Name and address of receiving party(ies)

Name: Sumitomo Mitsubishi Silicon Corporation

Internal Address: _____

Street Address: _____

Sumitomo Mitsubishi Silicon Corporation
2-1, Shibaura 1-chome, Minato-kuCity: Tokyo

State: _____

Country: Japan Zip: 105-0023Additional name(s) & address(es) attached: ☐ Yes ☒ No

4. Application or patent number(s):

A. Patent Application No.(s)

10/512,405

☐ This document is being filed together with a new application.

B. Patent No.(s)

Additional numbers attached?

☐ Yes ☒ No

5. Name and address to whom correspondence concerning document should be mailed:

Name: Kevin R. Spivak
MORRISON & FOERSTER LLPInternal Address: Atty. Dkt.: 474082002800Street Address: 1650 Tysons Blvd, Suite 30000000052 031952 10512405

40.00 DA

City: McLeanState: VA Zip: 22102Phone Number: (703) 760-7743Fax Number: (703) 760-7777Email Address: KSpivak@mpfo.com

6. Total number of applications and patents involved:

1

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00

- ☐ Authorized to be charged by credit card
☒ Authorized to be charged to deposit account
☐ Enclosed
☐ None required (government interest not affecting title)

8. Payment Information

a. Credit Card Last 4 Numbers _____
Expiration Date _____b. Deposit Account Number 03-1952Authorized User Name Kevin R. Spivak

9. Signature:

Signature

June 15, 2005

Date

Kevin R. Spivak - 43,148
Name of Person SigningTotal number of pages including cover
sheet, attachments, and documents:

3

va-118580

MORRISON & FOERSTER LLP

For good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the undersigned hereby assign(s), transfer(s) and set(s) over to:

Sumitomo Mitsubishi Silicon Corporation
2-1, Shibaura 1-chome, Minato-ku
Tokyo 105-0023
JAPAN

(hereinafter designated as the "ASSIGNEE"), its successors and assigns, the entire right, title, and interest for the United States in the invention, and all applications for patent and any Letters Patent which may be granted therefor, including said application, and all United States Letters Patent which may be granted thereof, and all divisions, reissues, continuations and extensions thereof, the said interest being the entire ownership of said Letters Patent when granted to be held by said ASSIGNEE, its successors, assigns or their legal representatives, to the full end of the term for which said Letters Patent may be granted, as fully and entirely as the same would have been and enjoyed by Assignor(s) if this assignment had not been made, the application being known by the title:

**HIGH-RESISTANCE SILICON WAFER AND ITS
MANUFACTURING METHOD**

Attorney Docket No.: 47408-20028.00

for which the undersigned has/have executed an application for patent in the United States of America on the same day herewith. Where this instrument is not filed concurrently with the application, the following identifying information may be added after execution:

Serial No.: 10/512,405

Filing Date: October 25, 2004

1. The undersigned hereby agree(s) to sign and execute any further documents and instruments which may be necessary, lawful and proper in the prosecution of said above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reexamination or reissue application or in any amendments, extension of interference proceedings, or otherwise to secure the title thereto to the ASSIGNEE.

2. The undersigned agree(s) to execute all papers and documents and perform any act which may be necessary in connection with claims or provisions of the International Convention for Protection of Industrial Property of similar agreements.

3. The undersigned agree(s) to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the ASSIGNEE.

4. The undersigned hereby authorize(s) and request(s) the Commissioner of Patents in the United States to issue any and all Letters Patent resulting from said application or any division or divisions or continuing applications thereof to the said ASSIGNEE.

5. The undersigned hereby grant(s) to the firm of Morrison & Foerster LLP the power to insert on this assignment any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

IN WITNESS WHEREBY, executed by the undersigned on the date(s) opposite the undersigned name(s).

06/06/2005

Date

Nobumitsu Takase

Typed Name: Nobumitsu TAKASE

06/03/2005

Date

Hideshi Nishikawa

Typed Name: Hideshi NISHIKAWA

06/06/2005

Date

Makoto Ito

Typed Name: Makoto ITO

06/08/2005

Date

Koji Sueoka

Typed Name: Koji SUEOKA

06/06/2005

Date

Shinsuke Sadamitsu

Typed Name: Shinsuke SADAMITSU



copy

Docket No.: 474082002800
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Nobumitsu TAKASE et al.

Application No.: 10/512,405

Confirmation No.: 4607

Filed: June 15, 2005

Art Unit: 2812

For: HIGH-RESISTANCE SILICON WAFER AND
ITS MANUFACTURING METHOD

Examiner: Not Yet Assigned

REQUEST FOR CORRECTED FILING RECEIPT

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Dear Sir:

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Respectfully submitted,

By 

Kevin R. Spivak

Registration No.: 43,148

MORRISON & FOERSTER LLP

1650 Tysons Blvd, Suite 300

McLean, Virginia 22102

(703) 760-7762



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
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25227
 MORRISON & FOERSTER LLP
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OC000000016356604

Date Mailed: 06/24/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

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 Hideshi Nishikawa, Tokyo, JAPAN;
 Makoto Ito, Tokyo, JAPAN;
 Koji Sueoka, Tokyo, JAPAN;
 Shinsuke Sadamitsu, Tokyo, JAPAN;

RECEIVED
 JUN 30 2005
 MORRISON & FOERSTER LLP

Assignment For Published Patent Application

SUMITOMO MITSUBISHI SILICON CORPORATION, Minato-ku, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25227.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/04866 04/16/2003

Foreign Applications

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 JAPAN 2002-360731 12/12/2002

Projected Publication Date: 09/29/2005

Non-Publication Request: No

Early Publication Request: No

DOCKETED

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GRANTED

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